

FIG. 1

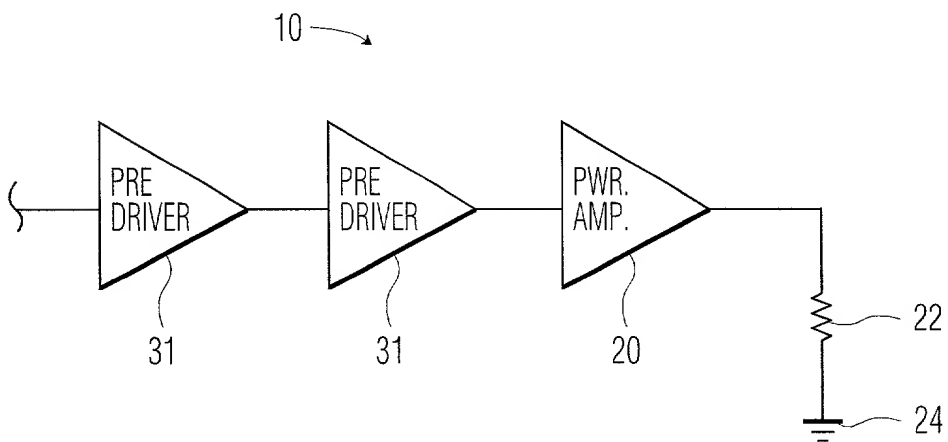


FIG. 2

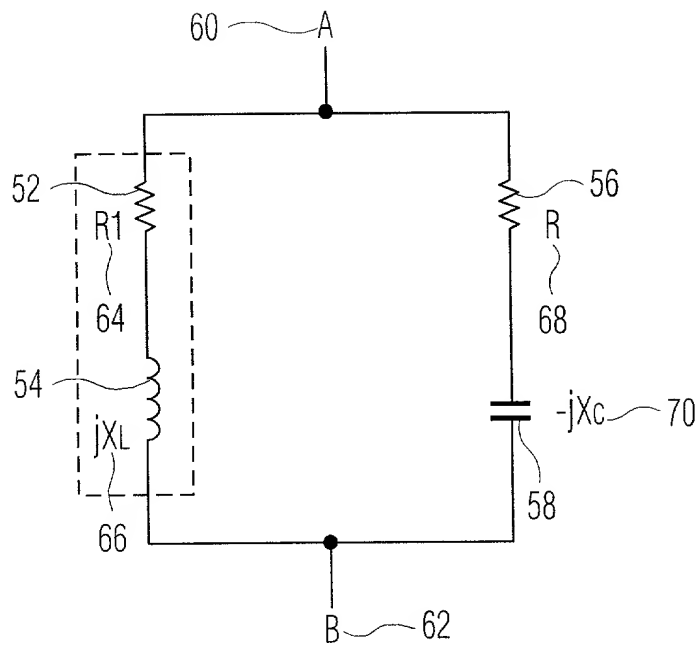


FIG. 3

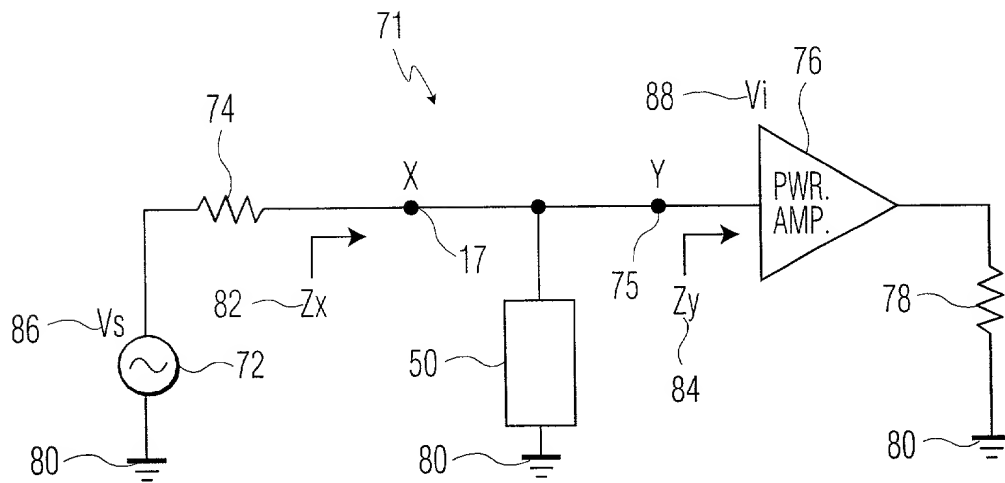


FIG. 4

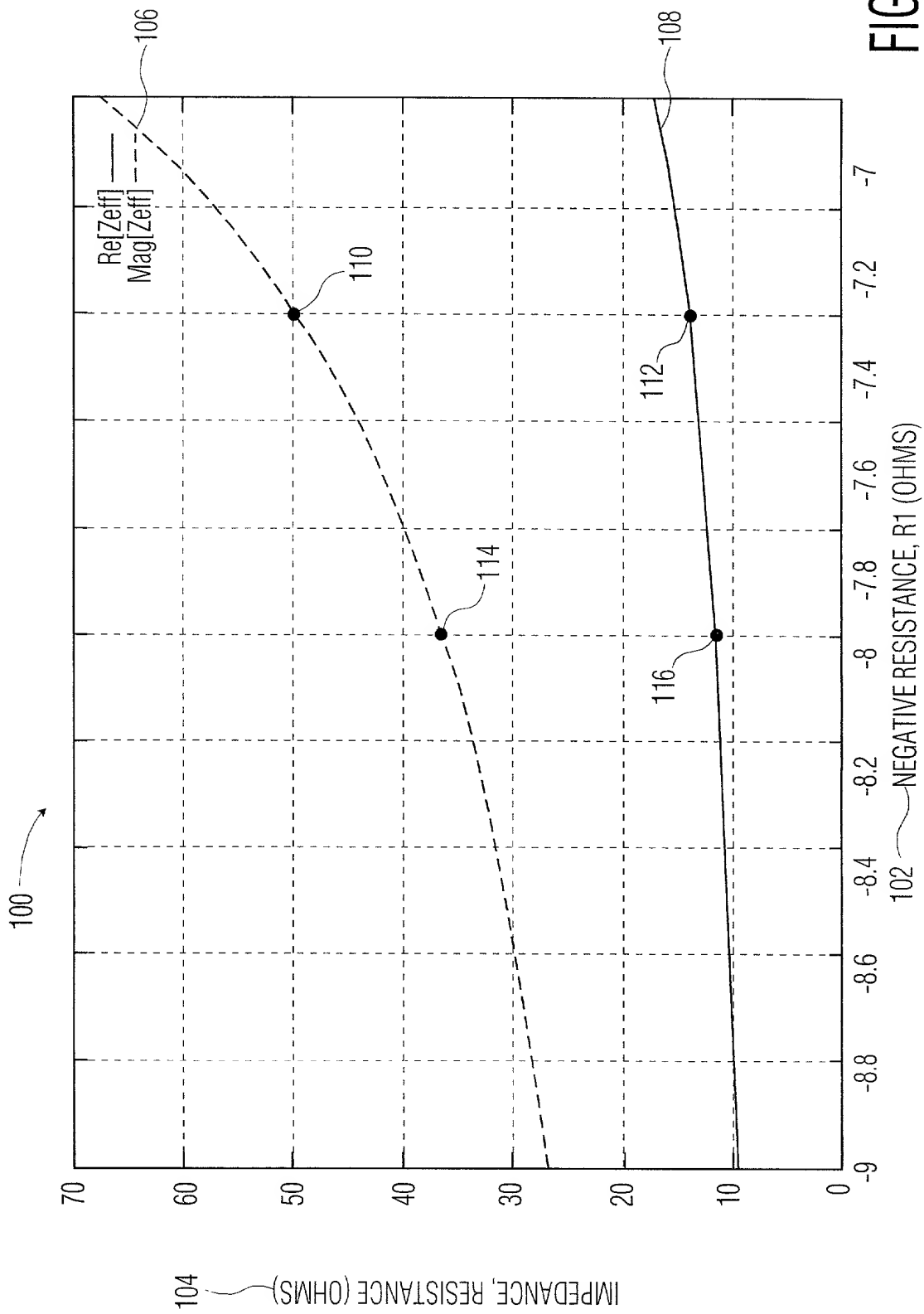


FIG. 5

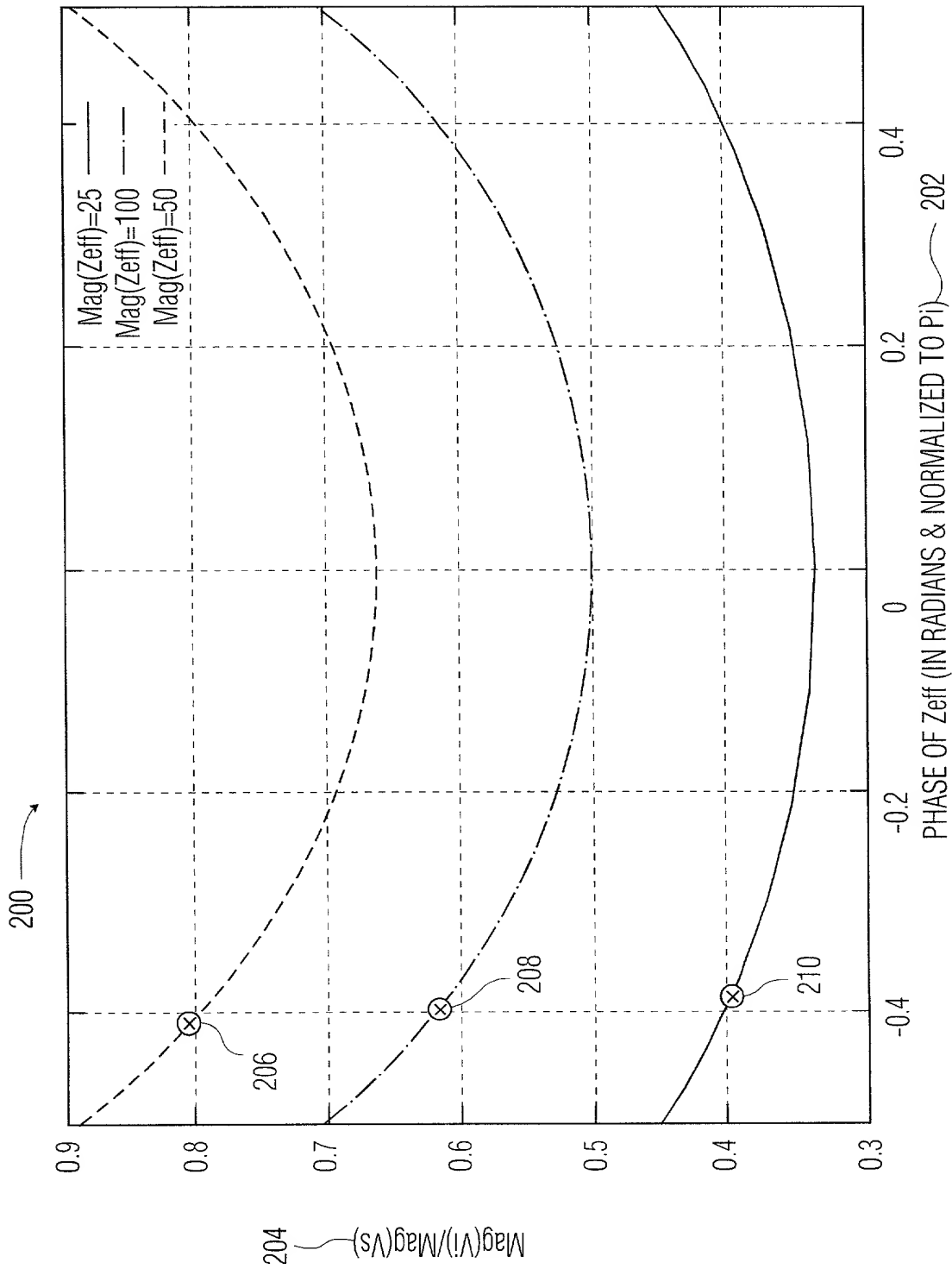


FIG. 6

300

PROVIDING A SIGNAL FROM
A SOURCE, WHEREIN THE
SIGNAL HAS A PREDETERMINED
IMPEDANCE.

302

JOINING AN IMPEDANCE
TRANSFORMER NETWORK IN
PARALLEL WITH THE SOURCE,
WHEREIN THE NETWORK
COMPRISES A NEGATIVE
RESISTOR IN SERIES WITH
AN INDUCTOR.

304

SELECTING A VALUE FOR THE
NEGATIVE RESISTOR SO THAT
THE PREDETERMINED
IMPEDANCE IS SYNTHESIZED
AT THE INPUT OF THE
POWER AMPLIFIER.

306

FIG. 7